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L1	(inquiry$3 near5 (command or instrucion) near5 opcode) same operands	28	L1

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L1 same (target or destination)	0

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	DB=PGPB,USPT,USOC; PLUR=YES; OP=OR		
L2	L1 same (target or destination)	1	L2
L1	(inquiry$3 near5 (command or instrucion) near5 opcode) same operands	28	L1

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Default operator: OR

☑ Plurals
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(inquiry$3 near10 (command or instruction) near10 opcode) same
operands
```

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	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	R
1	☐	☐	US 6804795 B1	20041012	30	Electronic device and its repairing method	714/10	701/33; 714/4	
2	☐	☐	US 6775714 B1	20040810	33	Communication method, communication apparatus,	710/8	370/431; 710/100;	
3	☐	☐	US 6751687 B1	20040615	40	Method of controlling device, transmission device,	710/62	348/734; 700/257;	

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1 **The need for effective biomedical imaging education**
Paschal, C.B.;
Engineering in Medicine and Biology Magazine, IEEE , Volume: 22 , Issue: 4 ,
Aug. 2003
Pages:88 - 91

[Abstract]   [PDF Full-Text (703 KB)]   **IEEE JNL**

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1 **Control unit synthesis targeting low-power processors**
Chuan-Yu Wang; Roy, K.;
Computer Design: VLSI in Computers and Processors, 1995. ICCD '95. Proceedings., 1995 IEEE International Conference on , 2-4 Oct. 1995
Pages:454 - 459

[Abstract]   [PDF Full-Text (576 KB)]   **IEEE CNF**

---

2 **Code compression techniques using operand field remapping**
Lin, K.; Chung, C.-P.;
Computers and Digital Techniques, IEE Proceedings- , Volume: 149 , Issue: 1 , Jan. 2002
Pages:25 - 31

[Abstract]   [PDF Full-Text (952 KB)]   **IEE JNL**

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3 **Efficient random vector verification method for an embedded 32-bit core**
Chang-Ho Lee; Hoon-Mo Yang; Sung-Ho Kwak; Moon-Key Lee; Sanghyun Par, Sangyeun Cho; Sangwoo Kim; Yongchun Kim; Seh-Woong Jeong; Bong-Youn₁ Chung; Hyung-Lae Roh;
ASICs, 2000. AP-ASIC 2000. Proceedings of the Second IEEE Asia Pacific Conference on , 28-30 Aug. 2000
Pages:291 - 294

[Abstract]   [PDF Full-Text (396 KB)]   **IEEE CNF**

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4 **Higher performance and lower power enhancements to VLIW architectures**
Gass, W.;

h   eee   e eee   g e ch e ch e   e   c e   e   ec   e

Signal Processing Systems, 2001 IEEE Workshop on , 26-28 Sept. 2001
Pages:157

[Abstract]   [PDF Full-Text (45 KB)]   **IEEE CNF**

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5 **DSP chips and total processing load of FFT analysis**
Nasir, B.M.;
DSP Chips in Real-Time Instrumentation and Display Systems (Digest No:
1997/300), IEE Colloquium on , 24 Sept. 1997
Pages:6/1 - 6/5

[Abstract]   [PDF Full-Text (216 KB)]   **IEE CNF**

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Code compression techniques using operand field remapping

Lin, K.   Chung, C.-P.
Dept. of Comput. Sci. & Inf. Eng., Nat. Chiao Tung Univ., Hsinchu, Taiwan
This paper appears in: **Computers and Digital Techniques, IEE Proceedings-**

Abstract:
Dictionary-based code compression stores the most frequently used **instruction** sequences in a dictionary and replaces the occurrences of these sequences in the program with codewords. The large dictionary size is due mainly to many **instruction** sequences which are different only in their operands, but are otherwise the same. The operand factorisation technique divides the expression tree into a tree pattern (**opcode** sequence) and an operand pattern (operand sequence) to reduce this redundancy. **Instruction** sequences with the same **opcodes** but different operands may thus share the same tree-pattern dictionary entry. This paper proposes an operand field remapping method to further reduce the dictionary size. The key idea is to explore the relations between the current operand to be compressed with those already compressed. The

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operand pattern dictionary is therefore divided into an operand remapping dictionary and an operand list dictionary. Each entry in the operand remapping dictionary indicates whether the operand (register or immediate value) to be compressed is the most-used operand, the same as the **destination** register of the previous **instructions,** or otherwise. With this remapping technique, the operand dictionary size is greatly reduced. An average 46% compression ratio can be achieved, where the compression ratio = (dictionary size + compressed code size)/(original program size)

Index Terms:
codes data compression dictionaries redundancy sequences tree data structures codewords compressed code size compression ratio dictionary size dictionary-based code compression techniques expression tree frequently used **instruction** sequences **instruction** sequences **opcode** sequence operand factorisation technique operand field remapping operand list dictionary operand pattern operand remapping dictionary operand sequence program size redundancy reduction register tree pattern

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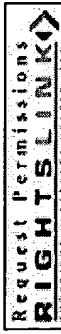
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Higher performance and lower power enhancements to VLIW architectures

Gass, W.
Texas Instrum. Inc., Dallas, TX, USA ;
This paper appears in: **Signal Processing Systems, 2001 IEEE Workshop on**

Meeting Date: 09/26/2001 - 09/28/2001
Publication Date: 26-28 Sept. 2001
Location: Antwerp Belgium
On page(s): 157
Number of Pages: viii+439
Inspec Accession Number: 7175572

Abstract:
Summary form only given. Architecture enhancements to the C6000 architecture have improved performance, reduced code size, lowered power, and increased compiler efficiency. Benchmarks of DSP kernels and typical DSP applications are used to compare commercially available DSP in terms of cycle count, power, and compiler efficiency. The C6000 VLIW family is an 8-issue **instruction** architecture that has four execution units for each of the two register banks. The C62x, first-generation processor runs at 300 MHz, has 2 multipliers, and dual 32-bit read/write ports. The 64x, second-generation processor extends the performance by increasing the speed to 600 MHz, adding 2 more multipliers

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and increasing the load/store width to 64-bits. In addition, the 64x adds SIMD operations to support packed data operations. The 62x is an excellent compiler **target** due to deterministic order and time of **instruction** execution, a general purpose 32-word register file, simple independent **instructions**, and no special modes or status bits. The 64x has improved the compiler efficiency by increasing the register file to 64 words, increasing the number of common **instructions** that will execute on each unit, and providing for non-aligned loads of packed data. The 64x reduces code size by decreasing the number of NOP with non-aligned program memory fetches and by adding complex **instructions** that combine several RISC **instructions** into one 32-bit **opcode.** The 64x reduces power by adding a 2-level on-chip cache, thereby enabling most of the memory accesses to hit the smaller first level cache. In addition, a reduction in code size decreases the number of first-level **instruction** fetches and the larger register file decreases the number of data memory accesses. The second-generation processor has been optimized for image, graphics, and telecommunication applications. For 2D algorithms such as 30 correlation, median filtering, motion estimation and polyphase filter, the cycle count improvements for the kernels range from 2.3x to 7.6x. For communication algorithms such as Reed Solomon decoding, Viterbi decoding and FFT, the cycle count improvements of the kernels range from 2.1 x to 3.5x

Index Terms:
cache storage   digital signal processing chips   instruction sets   parallel architectures   performance   evaluation   program compilers   reduced instruction set computing   2-level on-chip cache   2D   algorithms   300 MHz   32 bit   600 MHz   64 bit   C6000 VLIW family   DSP kernels   FFT   RISC   instructions   Reed Solomon decoding   SIMD operations   VLIW architectures   Viterbi decoding   compiler efficiency   complex instructions   correlation   graphics applications   image applications   independent instructions   instruction architecture   median filtering   motion estimation   multipliers   packed data operations   performance   polyphase filter   processor   read/write ports   telecommunication applications

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PGPUB-DOCUMENT-NUMBER: 20030070028
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030070028 A1

TITLE: Method and apparatus for utilizing extended AV/C command frames including status inquiry, notify inquiry and control inquiry command types

PUBLICATION-DATE: April 10, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Brelin, Jon Ebbe	Campbell	CA	US	

ASSIGNEE-INFORMATION:

NAME	CITY	STATE	COUNTRY	TYPE CODE
SONY CORPORATION				02

APPL-NO: 09/ 972488   [PALM]
DATE FILED: October 4, 2001

INT-CL: [07] G06 F 13/38

US-CL-PUBLISHED: 710/305
US-CL-CURRENT: 710/305

REPRESENTATIVE-FIGURES: 4

ABSTRACT:

A modified AV/C command set includes status inquiry, notify inquiry and control inquiry commands. The status inquiry, notify inquiry and control inquiry commands include an opcode and any number of operands. Thus, the status inquiry, notify inquiry and control inquiry commands can include only an opcode. The status inquiry, notify inquiry and control inquiry commands are sent from a controller to a target device to determine if the target device supports a particular status, notify or control command, respectively. In response to a status inquiry, notify inquiry or control inquiry command, a target device sends a response to the controller notifying the controller as to whether or not the target device supports the particular command.

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PGPUB-DOCUMENT-NUMBER: 20030070028
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030070028 A1

TITLE: Method and apparatus for utilizing extended AV/C command frames including
status inquiry, notify inquiry and control inquiry command types

PUBLICATION-DATE: April 10, 2003

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APPL-NO: 09/ 972488   [PALM]
DATE FILED: October 4, 2001

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US-CL-PUBLISHED: 710/305
US-CL-CURRENT: 710/305

REPRESENTATIVE-FIGURES: 4

ABSTRACT:

A modified AV/C command set includes status inquiry, notify inquiry and control
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inquiry, notify inquiry and control inquiry commands are sent from a controller to
a target device to determine if the target device supports a particular status,
notify or control command, respectively. In response to a status inquiry, notify
inquiry or control inquiry command, a target device sends a response to the
controller notifying the controller as to whether or not the target device supports
the particular command.

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L1: Entry 13 of 28   File: PGPB   Apr 25, 2002

PGPUB-DOCUMENT-NUMBER: 20020047862
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020047862 A1

TITLE: Network error display apparatus and error detection display method

PUBLICATION-DATE: April 25, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Aoki, Yukihiko	Tokyo		JP	
Takenaka, Yoshiaki	Tokyo		JP	
Miura, Haruyuki	Kanagawa		JP	
Ohashi, Shinobu	Ibaraki		JP	

APPL-NO: 09/ 941288   [PALM]
DATE FILED: August 28, 2001

FOREIGN-APPL-PRIORITY-DATA:

COUNTRY	APPL-NO	DOC-ID	APPL-DATE
JP	2000-259275	2000JP-2000-259275	August 29, 2000

INT-CL: [07] G09 G 5/00

US-CL-PUBLISHED: 345/736; 709/224
US-CL-CURRENT: 345/736; 709/224

REPRESENTATIVE-FIGURES: 20

ABSTRACT:

A problem is to propose a network error display apparatus and an error detection
display method capable of displaying a user's measure intelligibly while making a
distinction between an error of the receiving system on the network and an error
within a device. A network error display apparatus includes: an IEEE 1394 signal
processing section (4) for detecting an error of a loop state in an IEEE 1394
network, detecting a link state, detecting an error which occurs in such a state
that there is a link for the network or an error which occurs in such a state that
there is no link for the network; a main body processing section (5) for storing
messages indicating error states and conducting display processing on messages
stored on the basis of an error state; and a display section (6) for displaying a
message for a user. Therefore, a user's measure is displayed intelligibly while
making a distinction between an error of the receiving system on the network and an
error within a device.

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L1: Entry 13 of 28   File: PGPB   Apr 25, 2002

DOCUMENT-IDENTIFIER: US 20020047862 A1
TITLE: Network error display apparatus and error detection display method

Detail Description Paragraph:
[0077] The CTS indicates an ID of the command set. In the AV/C command set, CTS
="0000". A ctype/response field indicates a function class of a command when the
packet is a command, and a processing result of a command when the packet is a
response. Commands are broadly divided into four kinds of definition: (1) commands
(CONTROL) for controlling the function from the outside; (2) commands (STATUS) for
inquiring about the state from the outside; (3) commands for inquiring whether
support of a control command is present, from the outside (GENERAL INQUIRY (whether
support of an opcode is present) and SPECIFIC INQUIRY (whether supports of an
opcode and operands are present)); and (4) commands (NOTIFY) for requesting the
notice of a state change to the outside.

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L1: Entry 26 of 28   File: USPT   Oct 12, 2004

US-PAT-NO: 6804795
DOCUMENT-IDENTIFIER: US 6804795 B1

TITLE: Electronic device and its repairing method

DATE-ISSUED: October 12, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kawamura; Harumi	Tokyo			JP
Sato; Masahiko	Tokyo			JP

ASSIGNEE-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY	TYPE CODE
Sony Corporation	Tokyo			JP	03

APPL-NO: 09/ 701786   [PALM]
DATE FILED: May 7, 2001

FOREIGN-APPL-PRIORITY-DATA:

COUNTRY	APPL-NO	APPL-DATE
JP	11/096835	April 2, 1999

PCT-DATA:

APPL-NO	DATE-FILED	PUB-NO	PUB-DATE	371-DATE	102(E)-DATE
PCT/JP00/02155	April 2, 2000	WO00/60877	Oct 12, 2000		

INT-CL: [07] G06 F 11/00

US-CL-ISSUED: 714/10; 714/4, 701/33
US-CL-CURRENT: 714/10; 701/33, 714/4

FIELD-OF-SEARCH: 714/4, 714/10, 714/48, 701/33

PRIOR-ART-DISCLOSED:

U.S. PATENT DOCUMENTS

	PAT-NO	ISSUE-DATE	PATENTEE-NAME	US-CL
☐	4667330	May 1987	Kumagai	714/824
	5699505	December 1997	Srinivasan	714/31

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☐	<u>6023268</u>	February 2000	Britt et al.	709/203
☐	<u>6169725</u>	January 2001	Gibbs et al.	370/216
☐	<u>6259442</u>	July 2001	Britt et al.	345/721
☐	<u>6446201</u>	September 2002	Gunther	713/1
☐	<u>6467065</u>	October 2002	Mendez et al.	714/800
☐	<u>6615376</u>	September 2003	Olin et al.	714/57

FOREIGN PATENT DOCUMENTS

FOREIGN-PAT-NO	PUBN-DATE	COUNTRY	US-CL
0 756 276	January 1997	EP	
0 841 776	May 1998	EP	

ART-UNIT: 2113

PRIMARY-EXAMINER: Beausoliel; Robert

ASSISTANT-EXAMINER: Duncan; B

ATTY-AGENT-FIRM: Frommer Lawrence & Haug LLP Frommer; William S. Smid; Dennis M.

ABSTRACT:

In a system in which a plurality of devices are connected by using a digital interface bus, in order to readily and easily cope with a specific device when the specific device malfunctions, the system, in compliance with a predetermined digital interface in which a predetermined identification code, a processing target discriminating code, a processing field prescribing code and processing executing information are set, includes means 12, 23 for recognizing the identification code, means 14, 22 for discriminating the processing target when the identification code is recognized and information processing means 16 to 19, 24, 25 for executing the processing in accordance with the processing field if the processing target contains its own device, wherein a defective device can be specified and repaired with ease.

12 Claims, 28 Drawing figures

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    L1: Entry 28 of 28   File: USPT   Jun 15, 2004

US-PAT-NO: 6751687
DOCUMENT-IDENTIFIER: US 6751687 B1

TITLE: Method of controlling device, transmission device, and medium

DATE-ISSUED: June 15, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Sato; Makoto	Tokyo			JP
Takaku; Yoshiyuki	Tokyo			JP
Kaibuki; Futoshi	Tokyo			JP
Horiguchi; Mari	Kanagawa			JP
Sato; Naoyuki	Tokyo			JP
Kageyama; Yuichi	Kanagawa			JP

ASSIGNEE-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY	TYPE CODE
Sony Corporation	Tokyo			JP	03

APPL-NO: 09/ 857279   [PALM]
DATE FILED: February 8, 2002

FOREIGN-APPL-PRIORITY-DATA:

COUNTRY	APPL-NO	APPL-DATE
JP	11/283452	October 4, 1999

PCT-DATA:

APPL-NO	DATE-FILED	PUB-NO	PUB-DATE	371-DATE	102(E)-DATE
PCT/JP00/06914	October 4, 2000	WO01/26295	Apr 12, 2001		

INT-CL: [07] G06 F 13/00, G06 F 13/14

US-CL-ISSUED: 710/62; 710/5, 710/8, 700/257, 348/734
US-CL-CURRENT: 710/62; 348/734, 700/257, 710/5, 710/8

FIELD-OF-SEARCH: 710/5, 710/8, 710/62, 700/257, 348/734

PRIOR-ART-DISCLOSED:

U.S. PATENT DOCUMENTS

| Search Selected | | Search ALL | | Clear |

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	PAT-NO	ISSUE-DATE	PATENTEE-NAME	US-CL
☐	5552917	September 1996	Darbee et al.	398/107
☐	6285357	September 2001	Kushiro et al.	345/169

FOREIGN PATENT DOCUMENTS

FOREIGN-PAT-NO	PUBN-DATE	COUNTRY	US-CL
1212422	March 1999	CN	
198 42 639	March 1999	DE	
2 768 844	March 1999	FR	
11-96735	April 1999	JP	
11-205313	July 1999	JP	

ART-UNIT: 2182

PRIMARY-EXAMINER: Perveen; Rehana

ATTY-AGENT-FIRM: Frommer Lawrence & Haug LLP Frommer; William S. Simon; Darren M.

ABSTRACT:

Between a first equipment and a second equipment connected to a predetermined bus line, it is arranged that data for setting, etc. on input selection in the first equipment is instructed from the second equipment in the form of command having a predetermined format through the bus line, thus allowing an independent remote control to be performed between the first equipment and the second equipment. Moreover, data on input selection in the first equipment is transmitted to the second equipment, so that, for example, the remote control of the input selection in the first equipment linking with the operation of the second equipment will be performed satisfactorily.

6 Claims, 38 Drawing figures

h   e b   b g e e e f   c   e   e   ge

EAST Browser - L1: (3) (inquiry$3 n... | US 6804795 | Tag: S | Doc: 1/3 | "Front Page" 1/1 (Total Images 30) | Front Page

File   Edit   View   Tools   Window   Help

US006804795B1

(12) United States Patent
Kawamura et al.

(10) Patent No.: **US 6,804,795 B1**

(45) Date of Patent: **Oct. 12, 2004**

(54) **ELECTRONIC DEVICE AND ITS REPAIRING METHOD**

(75) Inventors: **Harumi Kawamura**, Tokyo (JP); **Masahiko Sato**, Tokyo (JP)

(73) Assignee: **Sony Corporation**, Tokyo (JP)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

(21) Appl. No.: **09/701,786**

(22) PCT Filed: **Apr. 2, 2000**

(86) PCT No.: **PCT/JP00/02155**

§ 371 (c)(1),
(2), (4) Date: **May 7, 2001**

(87) PCT Pub. No.: **WO00/60877**

PCT Pub. Date: **Oct. 12, 2000**

(30)   **Foreign Application Priority Data**

Apr. 2, 1999   (JP) 11/096835

(51) Int. Cl.^{7} G06F 11/00

(52) U.S. Cl. 714/10; 714/4; 701/33

(58) Field of Search 714/4, 10, 48; 701/33

(56)   **References Cited**

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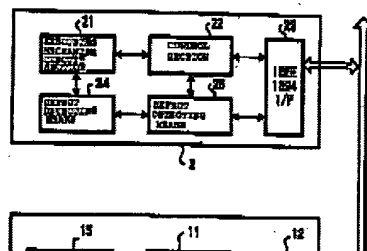|		
EP	0 756 276	1/1997
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Primary Examiner—Robert Beausoliel
Assistant Examiner—B Duncan
(74) *Attorney, Agent, or Firm*—Frommer Lawrence & Haug LLP; William S. Frommer; Dennis M. Smid

(57)   **ABSTRACT**

In a system in which a plurality of devices are connected by using a digital interface bus, in order to readily and easily cope with a specific device when the specific device malfunctions, the system, in compliance with a predetermined digital interface in which a predetermined identification code, a processing target discriminating code, a processing field prescribing code and processing executing information are set, includes means 12, 23 for recognizing the identification code, means 14, 22 for discriminating the processing target when the identification code is recognized and information processing means 16 to 19, 24, 25 for executing the processing in accordance with the processing field if the processing target contains its own device, wherein a defective device can be specified and repaired with ease.

12 Claims, 19 Drawing Sheets

EAST Browser - L1: (3) (inquiry$3 n... | US 6751687 | Tag: S | Doc: 3/3 | "Front Page" 1/1 (Total Images 40) | Front Page

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US006751687B1

(12) United States Patent

Sato et al.

(10) Patent No.: **US 6,751,687 B1**

(45) Date of Patent: **Jun. 15, 2004**

(54) **METHOD OF CONTROLLING DEVICE, TRANSMISSION DEVICE, AND MEDIUM**

(75) Inventors: **Makoto Sato**, Tokyo (JP); **Yoshiyuki Takaku**, Tokyo (JP); **Futoshi Kaibuki**, Tokyo (JP); **Mari Horiguchi**, Kanagawa (JP); **Naoyuki Sato**, Tokyo (JP); **Yuichi Kageyama**, Kanagawa (JP)

(73) Assignee: **Sony Corporation**, Tokyo (JP)

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 400 days.

(21) Appl. No.: **09/857,279**

(22) PCT Filed: **Oct. 4, 2000**

(86) PCT No.: **PCT/JP00/06914**

§ 371 (c)(1),
(2), (4) Date: **Feb. 8, 2002**

(87) PCT Pub. No.: **WO01/26295**

PCT Pub. Date: **Apr. 12, 2001**

(30)   **Foreign Application Priority Data**

Oct. 4, 1999   (JP) 11/283452

(51) Int. Cl.^{7} G06F 13/00; G06F 13/14
(52) U.S. Cl. 710/62; 710/5; 710/8; 700/257; 348/734
(58) Field of Search 710/5, 8, 62; 700/257; 348/734

(56)   **References Cited**

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DE	198 42 639 A1	3/1999
FR	2 768 844	3/1999
JP	11-96735	4/1999
JP	11-205313	7/1999

* cited by examiner

Primary Examiner—Rehana Perveen
(74) *Attorney, Agent, or Firm*—Frommer Lawrence & Haug LLP; William S. Frommer; Darren M. Simon

(57)   **ABSTRACT**

Between a first equipment and a second equipment connected to a predetermined bus line, it is arranged that data for setting, etc. on input selection in the first equipment is instructed from the second equipment in the form of command having a predetermined format through the bus line, thus allowing an independent remote control to be performed between the first equipment and the second equipment. Moreover, data on input selection in the first equipment is transmitted to the second equipment, so that, for example, the remote control of the input selection in the first equipment linking with the operation of the second equipment will be performed satisfactorily.

6 Claims, 26 Drawing Sheets
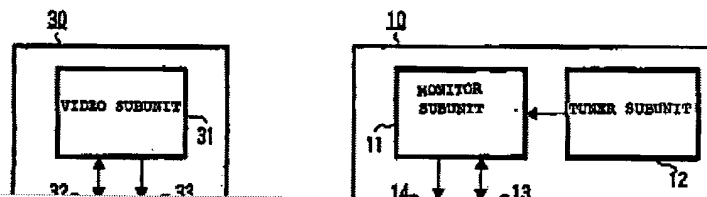
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L1: Entry 1 of 1   File: USPT   Jun 29, 2004

US-PAT-NO: 6757773
DOCUMENT-IDENTIFIER: US 6757773 B1

TITLE: System and method for determining support capability of a device coupled to a bus system

DATE-ISSUED: June 29, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Brelin; Jon Ebbe	San Jose	CA		

ASSIGNEE-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY	TYPE CODE
Sony Corporation	Tokyo			JP	03
Sony Electronics Inc.	Park Ridge	NJ			02

APPL-NO: 09/ 608121   [PALM]
DATE FILED: June 30, 2000

INT-CL: [07] G06 F 13/14

US-CL-ISSUED: 710/305; 710/5
US-CL-CURRENT: 710/305; 710/5

FIELD-OF-SEARCH: 710/305, 710/105, 710/100, 710/5, 710/52, 707/3, 707/100

PRIOR-ART-DISCLOSED:

U.S. PATENT DOCUMENTS

[Search Selected] [Search ALL] [Clear]

	PAT-NO	ISSUE-DATE	PATENTEE-NAME	US-CL
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☐	4935894	June 1990	Ternes et al.	710/128
☐	5381138	January 1995	Stair et al.	340/825.44
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☐	5485505	January 1996	Norman et al.	379/58
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"Fire on the Wire: The IEEE 1934 High Performance Serial Bus", The IEEE Microcomputer Standards Committee, 1986.
http://www.chumpchange.com/parkplace/video/dvpapers/firewire.html (4 pgs.).

ART-UNIT: 2833

PRIMARY-EXAMINER: Auve; Glenn A.

ASSISTANT-EXAMINER: Chung-Trans; X.

ATTY-AGENT-FIRM: Blakely, Sokoloff, Taylor & Zafman LLP

ABSTRACT:

A system and method for determining support capability of a device coupled to a bus system are described. A command is received from a device, the command requesting support capability information. The command is parsed to extract a plurality of command fields. A response is then transmitted based on the plurality of command fields extracted.

58 Claims, 8 Drawing figures

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Search Results - Record(s) 1 through 8 of 8 returned.

☐ 1. Document ID: US 20030070028 A1

Using default format because multiple data bases are involved.

L3: Entry 1 of 8   File: PGPB   Apr 10, 2003

PGPUB-DOCUMENT-NUMBER: 20030070028
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030070028 A1

TITLE: Method and apparatus for utilizing extended AV/C command frames including status inquiry, notify inquiry and control inquiry command types

PUBLICATION-DATE: April 10, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Brelin, Jon Ebbe	Campbell	CA	US	

US-CL-CURRENT: 710/305

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De

☐ 2. Document ID: US 20020073256 A1

L3: Entry 2 of 8   File: PGPB   Jun 13, 2002

PGPUB-DOCUMENT-NUMBER: 20020073256
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020073256 A1

TITLE: Electronic equipment, and method for controlling state of physical layer circuit therefor

PUBLICATION-DATE: June 13, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Nakamura, Akira	Tokyo		JP	
Sato, Tetsuya	Kanagawa		JP	

US-CL-CURRENT: 710/104; 710/305

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`Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De`

☐ 3. Document ID: US 20020047862 A1

L3: Entry 3 of 8   File: PGPB   Apr 25, 2002

PGPUB-DOCUMENT-NUMBER: 20020047862
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020047862 A1

TITLE: Network error display apparatus and error detection display method

PUBLICATION-DATE: April 25, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Aoki, Yukihiko	Tokyo		JP	
Takenaka, Yoshiaki	Tokyo		JP	
Miura, Haruyuki	Kanagawa		JP	
Ohashi, Shinobu	Ibaraki		JP	

US-CL-CURRENT: 345/736; 709/224

`Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De`

☐ 4. Document ID: US 20020046311 A1

L3: Entry 4 of 8   File: PGPB   Apr 18, 2002

PGPUB-DOCUMENT-NUMBER: 20020046311
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020046311 A1

TITLE: Communications controlling method, communications system, and communications device

PUBLICATION-DATE: April 18, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Kageyama, Yuichi	Kanagawa		JP	

US-CL-CURRENT: 710/105

`Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De`

☐ 5. Document ID: US 20020010824 A1

L3: Entry 5 of 8   File: PGPB   Jan 24, 2002

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PGPUB-DOCUMENT-NUMBER: 20020010824
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020010824 A1

TITLE: Electronic equipment and method for processing digital serial data at bus
initialization phase in interface unit

PUBLICATION-DATE: January 24, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Okawa, Sumihiro	Kanagawa		JP	
Miura, Kiyoshi	Kanagawa		JP	

US-CL-CURRENT: 710/305

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 6. Document ID: US 20010047440 A1

L3: Entry 6 of 8   File: PGPB   Nov 29, 2001

PGPUB-DOCUMENT-NUMBER: 20010047440
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20010047440 A1

TITLE: Information processing device, control device, information processing
system, and methods thereof

PUBLICATION-DATE: November 29, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Takaku, Yoshiyuki	Tokyo		JP	
Horiguchi, Mari	Tokyo		JP	

US-CL-CURRENT: 710/62

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 7. Document ID: US 6775714 B1

L3: Entry 7 of 8   File: USPT   Aug 10, 2004

US-PAT-NO: 6775714
DOCUMENT-IDENTIFIER: US 6775714 B1

TITLE: Communication method, communication apparatus, communication system and
providing medium

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`Full | Title | Citation | Front | Review | Classification | Date | Reference |        |        | Claims | KWIC | Draw De`

☐ 8. Document ID: US 6751687 B1

L3: Entry 8 of 8   File: USPT   Jun 15, 2004

US-PAT-NO: 6751687
DOCUMENT-IDENTIFIER: US 6751687 B1

TITLE: Method of controlling device, transmission device, and medium

`Full | Title | Citation | Front | Review | Classification | Date | Reference |        |        | Claims | KWIC | Draw De`

| Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS |

Terms	Documents
L1 and L2	8

Display Format: `-`   Change Format

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